

TI 33069

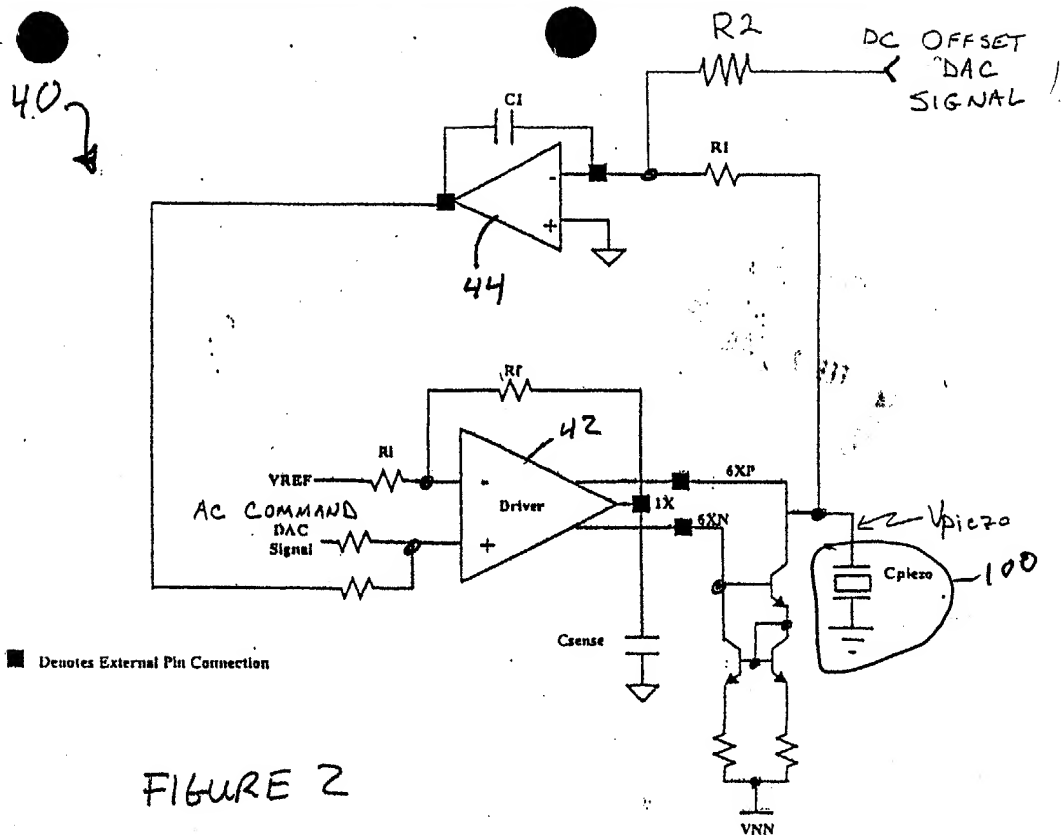


FIGURE 2

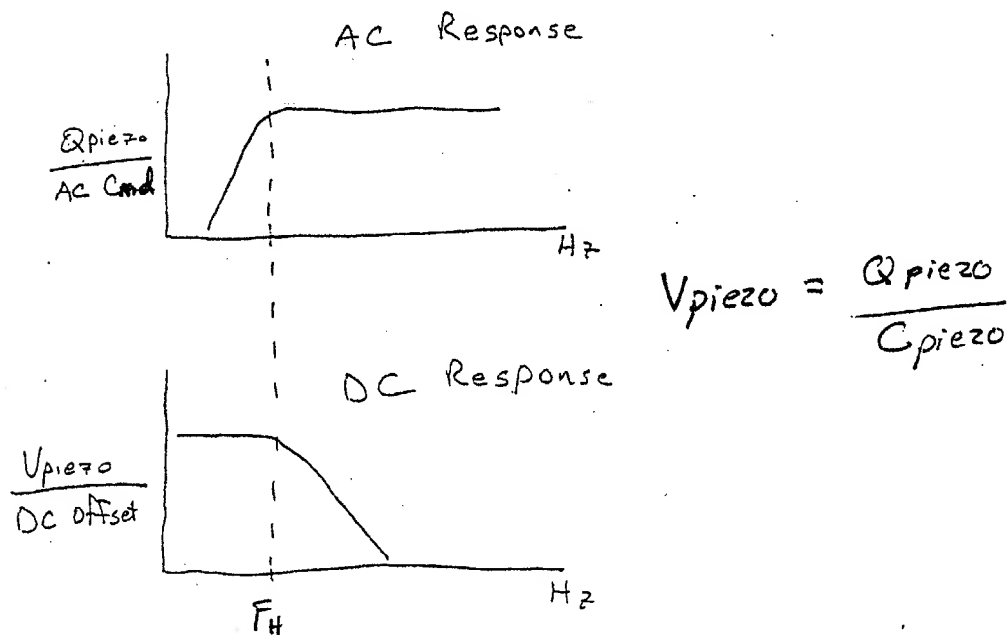


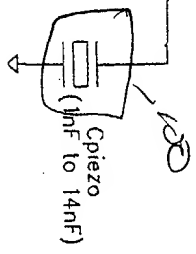
FIGURE 3

40



50

44



F-16W2E 6

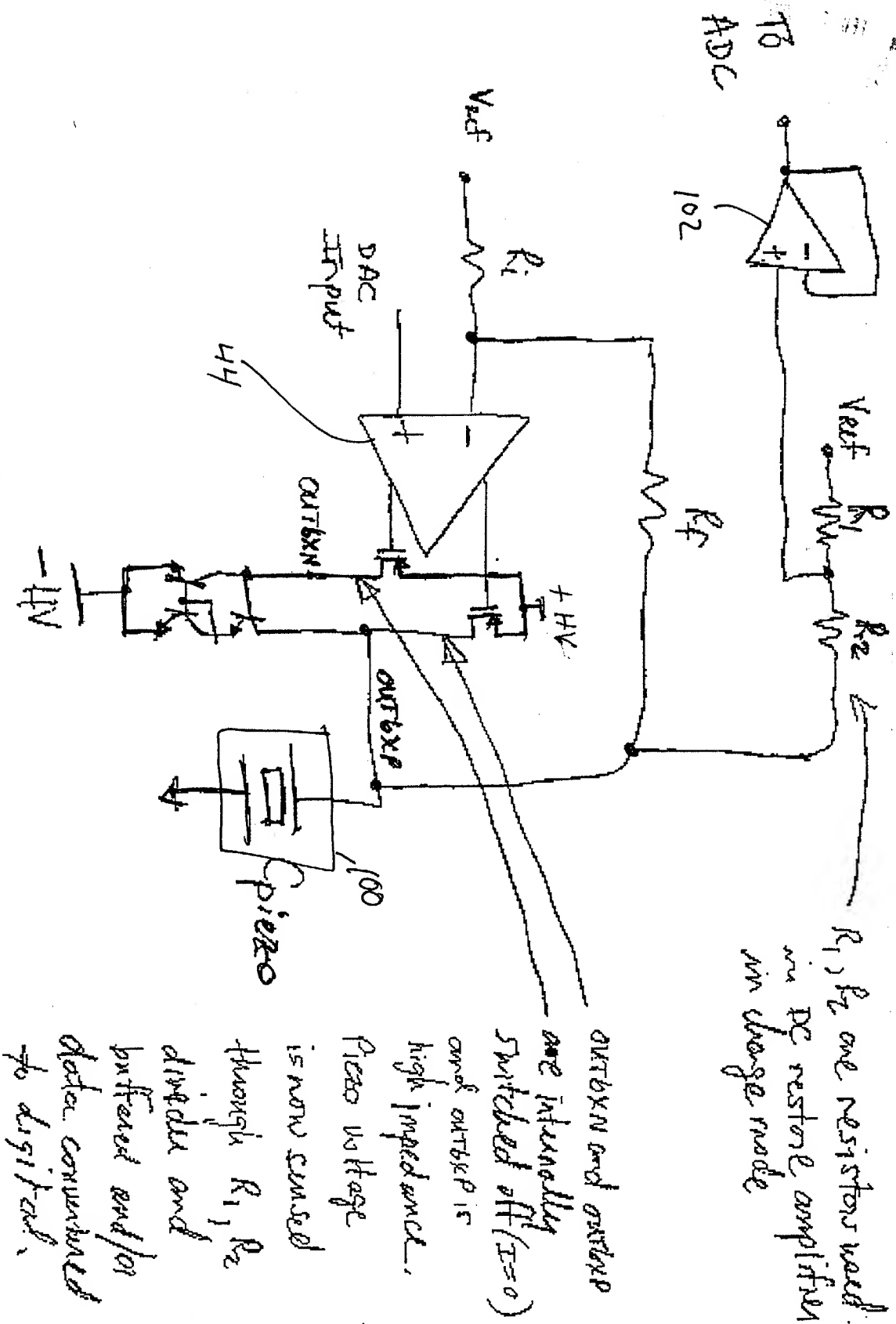


Figure 10 Voltage Mode Output Stage

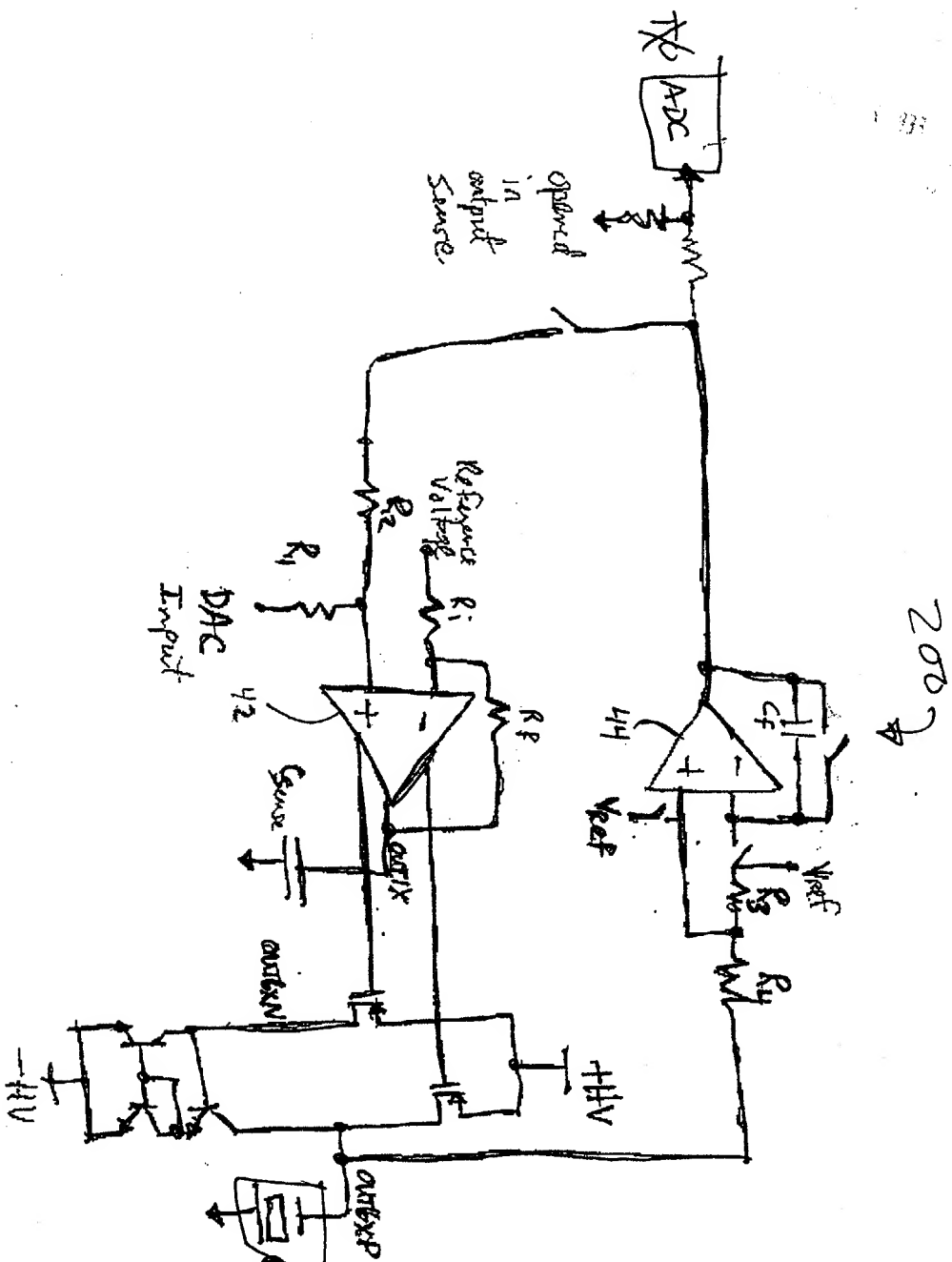


Figure 12: Charge Mode Output Sense

In charge mode, the output sense, the DC restore amplifier is reconfigured. The feedback capacitor is shunted, the R_3, R_4 feedback is opened and connected to V_{ref} . The non-inverting input is opened from V_{ref} and connected to the R_3, R_4 divider.

The outputs $outw$ and $outxp$ are made high impedance and output is to be sensed through R_3, R_4 to reconfigure DC restore amplifier and sent to ADC.